Roll No. ....   **Total Pages : 02**

BT-4/M-20   **34125**

FUNDAMENTALS OF MICROPROCESSOR INTERFACING AND APPLICATION
IT-210N

Time : Three Hours]   [Maximum Marks : 75

Note : Attempt *Five* questions in all, taking at least question from each of the four Sections.

Section I

1. Explain using a block diagram the architecture of 8085 microprocessor.   **15**

2. Draw and explain the timing diagram of MVI A, 32 H instruction. The instruction is stored at memory location 2000 H.   **15**

Section II

3. With the help of suitable examples, explain the various addressing modes of 8085 microprocessor. **15**

4. With the help of suitable example, explain the operation of PUSH and POP instruction on the stack. **15**

Section III

5. Compare Memory mapped I/O and peripheral I/O scheme. **15**

6. Explain the various interrupts of 8085. Also write vector location of each interrupt. **15**

Section IV

7. Explain the internal architecture of Programmable Peripheral Interface 8255 with the help of block diagram. **15**

8. With the help of block diagram explain Microprocessor Controlled Temperature System (MCTS). **15**